

Things to Remember

- Place ground/power planes on opposite sides of board unless you have a reason not to
- Place decoupling capacitors close to VCC input of MCU
- Clock line symmetry
- Try to route top traces horizontally and bottom vertically, or vice versa (at right angles)
- Label things if necessary

Shortcuts for Layout

Shortcut	Function
PT	To place trace between pins
EOS	place origin
PL	place line (do on mechanical layer for board outline) Then Design -> Board Shape -> Define from selected objects
NHA/NSA	Net Hide All / Net Show All (hide and show net nest) You can also hide/show just a particular net - this is useful for gnd/power
PV	Place Via
EGA	Align components (or text, vias, etc)
Space Bar	Rotate while holding a component - must be dragging component
L	Switch component to opposite side of board (top vs bottom) - must be dragging component
Shift-S	Highlight layer
2/3	See in 2D/3D
TDR	Run Design Rule Check (DRC)
TGE/TGH	Show and hide polygon pours