Seek for Highly-Motivated Interns Working on Efficient Machine Learning Hardware

**Why us?**
- Pioneered research in efficient ML hardware (architecture, FPGA, chip, etc.)
  - 19 papers in top-tier hardware conferences in recent 3 years

**Why you?**
- Self-motivated and hardworking
- Knowing FPGA and popular architectures of Neural Network accelerators
- Experience in implementation of some basic components (e.g., conv)

**What could you earn?**
- Opportunities to work in research projects and publish papers in top-tier conferences and journals
- Collaborations with big tech. companies, including Meta/IBM/Microsoft
- Strong recommendation for your MS/PhD/job application

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**Efficient Algorithms**
**Efficient Architecture**
Towards Ubiquitous ML-Powered Intelligent Devices and Green AI

**Automated Tools**

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**Recent Projects (Click to view more information)**
- Efficient Neural Synthesis: RT-NeRF
- Intelligent VR/AR Camera: i-FlatCam
- Efficient Model: ShiftAddNAS

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Efficient and Intelligent Computing (EIC) Lab @ Georgia Tech.

Scan the QR Code to apply

Scan the QR Code to visit our website:
http://eiclab.scs.gatech.edu/