

main.asm

```
1;-----  
-----  
2; MSP430 Assembler Code Template for use with TI Code Composer  
  Studio  
3;  
4;  
5;-----  
-----  
6          .cdecls C,LIST,"msp430.h"          ; Include device header  
  file  
7  
8;-----  
-----  
9          .def      RESET                    ; Export program entry-  
  point to  
10                                     ; make it known to  
  linker.  
11;-----  
-----  
12          .text                             ; Assemble into program  
  memory.  
13          .retain                             ; Override ELF  
  conditional linking  
14                                     ; and retain current  
  section.  
15          .retainrefs                         ; And retain any  
  sections that have  
16                                     ; references to current  
  section.  
17  
18;-----  
-----  
19 RESET      mov.w    #__STACK_END,SP        ; Initialize  
  stackpointer  
20 StopWDT    mov.w    #WDTPW|WDTHOLD,&WDTCTL ; Stop watchdog timer  
21  
22;-----  
-----  
23; Main loop here  
24;-----
```

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```
-----
25
26 ; Configure RED LED, LED1 (Port 1, Pin 0)
27     bis.b    #BIT0, &P1OUT
28     bis.b    #BIT0, &P1DIR           ; Set P1.0 as output pin
29
30 ; Configure Green LED, LED2 (Port 9, Pin 7)
31     bis.b    #BIT7, &P9OUT
32     bis.b    #BIT7, &P9DIR           ; Set P9.7 as output pin
33
34 ; Configure Push Button S1 (Port 1, Pin 1)
35     bis.b    #BIT1, &P1REN           ; Enable Resistor
36     bis.b    #BIT1, &P1OUT           ; Pull-up resistor,
    since S1 is active low
37     bis.b    #BIT1, &P1IE           ; Enable P1.1 interrupts
38     bis.b    #BIT1, &P1IES          ; Enable Loweing Edge
39
40 ; Configure Push Button S2 (Port 1, Pin 2)
41     bis.b    #BIT2, &P1REN           ; Enable Resistor
42     bis.b    #BIT2, &P1OUT           ; Pull-up resistor,
    since S2 is active low
43     bis.b    #BIT2, &P1IE           ; Enable P1.2 interrupts
44     bis.b    #BIT2, &P1IES          ; Enable Loweing Edge
45
46     bic.w    #LOCKLPM5,&PM5CTL0     ; Disable the GPIO
    power-on default
47                                     ; high-impedance mode
48
49     nop
50     bis.w    #GIE|LPM3,    SR       ; sleep and wait for
    interrupts
51     nop
52
53 ; -----          ISR
-----
54
55 PORT1_ISR:
56
57     bit.b    #BIT1, &P1IFG
58     jnc IF1_EXIT
```

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```
59          xor.b    #BIT0,      &P1OUT      ; Toggle P1.0
60          ;bic.b    #BIT1, &P1IFG
61 IF1_EXIT:
62
63          bit.b    #BIT2, &P1IFG
64          jnc IF2_EXIT
65          xor.b    #BIT7,      &P9OUT      ; Toggle P9.7
66          ;bic.b    #BIT2, &P1IFG
67 IF2_EXIT:
68
69          ;clr.b    &P1IFG              ; multi-sourced,
        therefore clear IF (Interrupt Flag)
70          bic.b    #BIT1, &P1IFG
71          bic.b    #BIT2, &P1IFG
72          reti
73
74 ;-----
        -----
75 ; Stack Pointer definition
76 ;-----
        -----
77          .global __STACK_END
78          .sect   .stack
79
80 ;-----
        -----
81 ; Interrupt Vectors
82 ;-----
        -----
83 ; Connect PORT1 interrupts to Interrupt Service Routine
84          .sect   ".int37"              ; PORT1 Vector
85          .short  PORT1_ISR
86
87          .sect   ".reset"              ; MSP430 RESET Vector
88          .short  RESET
89
90
```