

Silicon Nanowire Array Photoelectrochemical Cells

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The development of efficient but inexpensive solar cells is an important current challenge. Semiconductor nanowires provide new opportunities for addressing the cost and efficiency issues of conventional photovoltaic (PV) and photoelectrochemical (PEC) cells. Recent studies of semiconductor nanowire growth^{1–3} show that it is relatively easy to make arrays of vertically oriented single crystals, which should be less susceptible to the grain-boundary recombination problem of polycrystalline solar cells. Moreover, relaxed lattice matching requirements in nanowires allow one to make epitaxial nanowire heterostructures,^{4–6} thereby enabling better solar spectral utilization with multi-band gap nanowires.

The “bed of nails” geometry of a vertical nanowire array also offers significant advantages over a planar semiconductor photoelectrode. One important consequence is the potential for all photo-generated carriers to be created within the space charge region of the semiconductor, regardless of the light penetration depth. The doping density and depletion layer width can thus be tailored independently of the absorption length.⁷ This geometry is also well suited to excitonic cells, in which vertical nanowires minimize exciton diffusion distances while providing an uninterrupted conduction pathway.^{8,9} For these reasons, semiconductor nanoarchitectures have been increasingly studied for their potential solar energy applications. Indeed, nanocrystal hybrid PVs⁹ and nanowire dye-sensitized solar cells^{10–13} have already been reported. To date, however, the only reported solar energy application of silicon nanowire (SiNW) arrays relies on a non-transferable wafer etching technique.¹⁴

We previously reported the growth of SiNW arrays using anodic aluminum oxide (AAO) membranes as templates,³ and demonstrated p- and n-type doping.^{15,16} In our first attempts to make liquid junction PECs by this technique, we electrodeposited 50 μm long Co nanowires in the membranes and capped them with 250 nm Au segments (Figure 1). Si nanowires were then grown from SiH₄ at 500 °C by the vapor–liquid–solid (VLS) method.¹⁷ The back metallic contact was insulated with a combination of glass tubing and epoxy, leaving only the nanowire-bearing face of the SiNW electrode exposed. After etching away the exposed Au catalyst from the tips of the nanowires using aqueous tri-iodide, the electrochemical response shown in Figure 1C was obtained. The SiNW photoelectrode was immersed in a glass vial containing a solution of 2 mM tris(2,2'-bipyridyl)ruthenium(II) hexafluorophosphate (Ru(bpy)₃²⁺) (bpy = 2,2'-bipyridyl) and 100 mM tetra(*n*-butyl)ammonium tetrafluoroborate (TBABF₄) in dry CH₃CN. A Ag/AgNO₃ (in CH₃CN) reference and a Pt gauze counter electrode completed the three-electrode cell. Ar was bubbled through the cell to deoxygenate the solution prior to electrochemical measurements, and a positive pressure of Ar was maintained at all times. A Hg/Xe arc lamp was used as the light source, yielding an

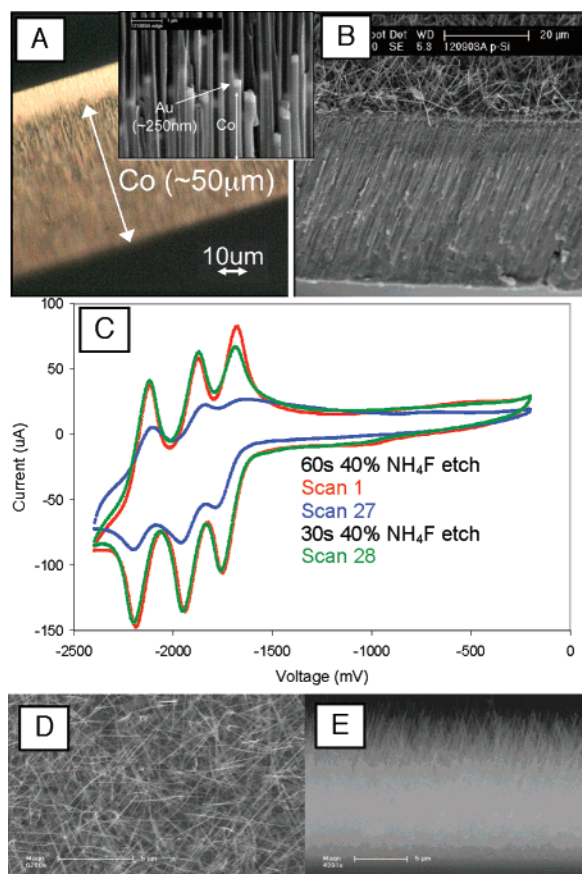


Figure 1. (A) Cross sectional view of AAO membrane showing the typical uniformity of electrodeposited Co. Inset SEM shows individual Co wires and their Au caps. (B) SEM showing a cross section of the membrane in panel A after VLS growth of p-Si nanowires, which protrude 10–15 μm beyond the top of the membrane. (C) Cyclic voltammetry of an illuminated p-Si/AAO nanowire array electrode in Ru(bpy)₃²⁺ solution, showing the effect of fluoride etching. Top (D) and side (E) view scanning electron micrographs of a free-standing p-Si nanowire array grown on Si.

illumination of 0.84 W/cm² at the cell surface. Surface oxide was etched from the SiNW array using aqueous ammonium fluoride.

In the case of the SiNW array grown in AAO, light and dark currents were identical (Figure 1C). Repeated voltammetric cycling covered the Si surfaces of the electrode with an insulating oxide, resulting in lower dark current. The recovery of this current with Etching indicates that neither exposed Au nor Co is the source of the dark current. Rather, it is likely that the SiNWs are degeneratively doped by Al from the AAO membrane, and therefore current rectification does not occur at the SiNW/solution interface.¹⁸

To grow SiNW arrays with lower doping density, a p-type (111) Si wafer with a 1 nm Au film was used as the substrate. Doping

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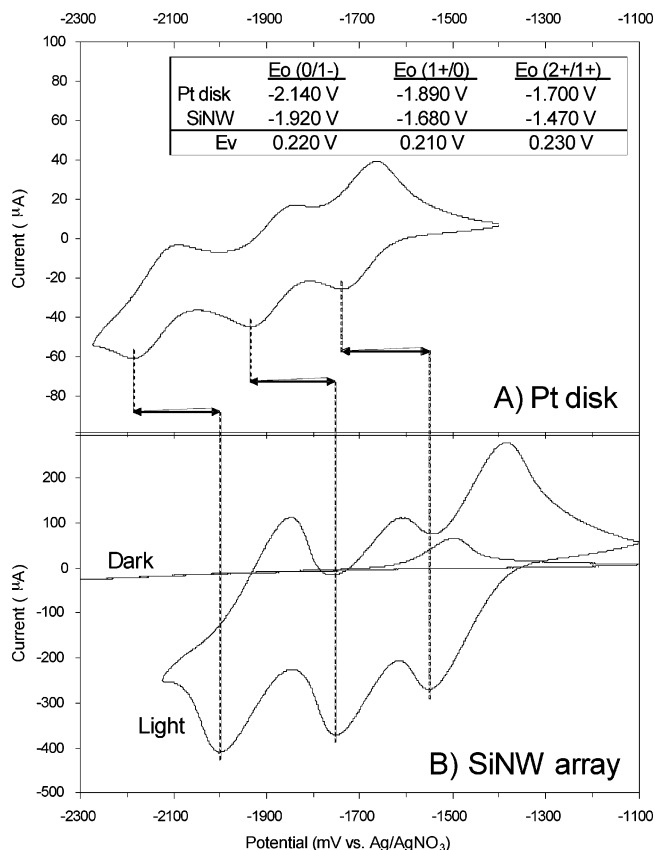


Figure 2. Cyclic voltammograms (200 mV/s) of (A) a Pt disk electrode and (B) a silicon nanowire array photocathode, both light and dark, as indicated. (Inset) Midpoint potentials (E_0) of the Ru(bpy)₃ anodic and cathodic peaks at the metallic and semiconductor electrodes and the calculated photovoltages (E_v).

was controlled by simultaneous introduction of trimethylboron (TMB) at a TMB/SiH₄ volume ratio of 2×10^{-4} . Micrographs of the resulting nanowires are shown in Figure 1D, E. The nanowires are approximately 14 μm long, with an average diameter of 57 ± 12 nm. Ohmic contact was made to the back side of the substrate via an evaporated Au film, which was then connected to a copper wire using conductive Ag epoxy.

Figure 2 shows cyclic voltammograms obtained from this p-SiNW array. Panel A shows the three cathodic and anodic peaks of the Ru(bpy)₃²⁺ redox couple at a Pt disk electrode. Panel B shows analogous i - V curves from the SiNW array photocathode, both illuminated and in the dark. Reduction of Ru(bpy)₃²⁺ by the illuminated SiNW array occurs at potentials significantly more positive than at the Pt electrode. In the dark, the SiNW array exhibits negligible cathodic current in the same potential range. Both these observations are consistent with the behavior of a p-type Si photocathode. The inset table lists the formal potentials (E_0) of Ru(bpy)₃²⁺ measured at the Pt disk and at the illuminated SiNW array, and the calculated photovoltages (E_v). E_0 values were measured as the mean of the peak potentials at each electrode; E_v values were calculated as the difference between corresponding E_0 values. On average, photoassisted reductions at the SiNW array occur 220 mV positive of the same reductions at the Pt disk electrode. At scan rates between 20 and 2000 mV/s, the photocurrent density at the SiNW array is approximately twice that of a planar p-Si electrode (see Supporting Information). This is consistent with photocurrent generation from the top $\sim 5 \mu\text{m}$ of the high surface area SiNW array.

In the mass transport limited regime of the SiNW PEC, the measured E_v values correspond to the open circuit voltage (V_{oc}) of the cell. Although the observation of a significant photovoltage is encouraging, a V_{oc} of ca. 500 mV is observed for p-Si single-crystal electrodes under similar conditions.^{19,20} Several explanations for the lower photovoltage of the nanowire photocathode are possible. One is that excessive doping results in an insufficiently thick space charge region, creating a leaky diode junction.²¹ This hypothesis is supported by data from nanowires made with higher TMB/SiH₄ ratios. At a ratio of 7×10^{-4} , the photovoltage was 130 mV, and at 2×10^{-2} , no photovoltage (i.e., identical light and dark current) was observed. It is also possible that the introduction of boron from TMB increases the density of surface states at the nanowire surface, resulting in increased dark current. It may be possible to minimize such surface effects by increasing the nanowire diameter, thus decreasing the surface to volume ratio. Accordingly, current efforts are focused on optimizing the doping levels and nanowire dimensions. The effects of surface states must also be considered.

To our knowledge, this is the first observation of photoelectrochemical energy conversion at a SiNW array grown by VLS or related techniques. As such, it represents a step toward the ultimate goal of making inexpensive SiNW PEC cells and multijunction solid-state devices.

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Supporting Information Available: Scan rate dependence of the photocurrent and additional synthetic and experimental details. This material is available free of charge via the Internet at <http://pubs.acs.org>.

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