Template Electrodeposition of Single-Phase p- and n-Type Copper Indium Diselenide (CuInSe₂) Nanowire Arrays

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ne-dimensional nano- and microstructures are attractive architectures for solar energy applications, both because of the unique physical properties that emerge at the nanoscale and because of geometrical effects that influence the performance of photovoltaic and photoelectrochemical devices.¹⁻⁵ In the vertical wire array geometry the length scale of light absorption and charge separation can be independently controlled. That is, the thickness required to absorb the light can be matched by the length of the wires, while charge separation occurs in the orthogonal direction throughout the wire. In this geometry the minority carriers travel distances equal to or less than the wire radius, which should decrease charge recombination, especially in semiconductors with short minority carrier diffusion lengths. The fact that the minority carriers travel only a short distance can provide more flexibility in terms of the doping and the crystallinity (single crystal vs polycrystalline) of the materials used. Lewis and co-workers demonstrated that electrodeposited CdSe nanowire array photoelectrodes have a higher minority carrier collection efficiency and absorb lower energy photons more efficiently compared to planar photoelectrodes of similar thickness.⁴ Also in a very elegant work, Fan and co-workers showed that a photovoltaic cell based on a n-CdS nanowires/p-CdTe junction had a higher efficiency ($\eta \sim 6\%$) than a planar CdS/CdTe film of the same thickness, which they attributed to more efficient minority carrier collection.⁵ In both of these experiments the nanowires were n-type. However it is more favorable in many applications to use p-type semiconductors in order to exploit the higher mobility and diffusion length of minority carrier electrons and to make photocathodes that are less susceptible to corrosion in liquid junctions.

ABSTRACT CulnSe₂ nanowire arrays were fabricated by electrodeposition from aqueous solutions of copper sulfate, indium sulfate, selenium dioxide, and citric acid, using anodic alumina membranes as templates. X-ray diffraction patterns showed that the wires were single phase (chalcopyrite structure) but polycrystalline, and a band gap of \sim 1 eV was obtained from optical measurements. TEM and SEM confirmed that the grain size in nanowires annealed at 400 °C was in the range of 40 nm. The composition of the nanowires was uniform along the length of the wires and could be tuned by varying the electrodeposition potential. Analysis by ICP-MS showed that naowires grown at -700 mV were slightly Cu-rich, whereas those grown at -750 mV were slightly In-rich. Mott–Schottky plots were employed to determine the doping type and flat band potential, verifying that the Cu- and In-rich wires were p- and n-type, respectively. Single-wire electrical transport measurements were also performed and showed that both types of wires had resistivities in the range $10^{-1}-10^{-3} \Omega \cdot cm$, consistent with carrier concentrations in the range $10^{18}-10^{20}$ cm⁻³.

KEYWORDS: CulnSe₂ · nanowires · Mott–Schottky · electrodeposition

Although p-type nanowires can be fabricated by vacuum techniques (i.e., VLS, CVD) and solution methods (i.e., SLS, hydrothermal), the former is expensive in terms of capital equipment, and the latter presents challenges in terms of the dimensions and the assembly of the nanowires. In this regard, template-assisted electrodeposition is a versatile technique that has several advantages for making nanowire arrays. First it provides an easy way to control the dimensions of the nanowires, as the diameter is set by the pore size and the length can be controlled by the electrodeposition time. Second, the wires can be left in the template for array measurements, or they can be released for single wire measurements and characterization. Third, it is an inexpensive technique and provides a high density of wires ($\sim 10^9$). However, most of the semiconductor nanowires reported in the literature that can be easily electrodeposited and have band gaps that match the solar spectrum are n-type.

Copper indium diselenide (CuInSe₂) has been extensively studied and widely used in solar cells. It has a direct band gap of 1 eV, * Address correspondence to tem5@psu.edu.

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Figure 1. XRD pattern of CulnSe_ nanowires: (a) as-deposited, (b) annealed at 400 $^\circ\text{C}.$

which matches the solar spectrum well, and it can be made by electrodeposition with n- or p-type doping. Photovoltaic cells based on polycrystalline films of CulnSe₂ have been reported with efficiencies as high as 15%.⁶ Several groups have reported the synthesis of nanostructures using solution routes and their use in solar devices.^{7–12} Phok *et al.* first reported the electrodeposition of CulnSe₂ nanowires by a pulse electrodeposition method.¹³ However they did not present full characterization of the wires, and no devices based on these nanowires were reported. Here we report the fabrication of p- and n-type CulnSe₂ nanowire arrays by a one-step electrodeposition route, as well as their full characterization.

RESULTS AND DISCUSSION

Figure 1 shows the XRD patterns of as-deposited and annealed CulnSe₂ nanowires embedded in the amorphous alumina membrane after dissolving the gold layer. The pattern corresponds to the chalcopyrite structure and can be indexed to the (112), (204/220), (116/312), and (400/008) crystal planes. For the asdeposited nanowires an average crystallite size was calculated from the Scherrer equation, eq 1, where λ is the X-ray wavelength in angstroms (1.54056 Å), β is the width of the XRD peak at half its height in radians, and θ_{β} is the position of the peak (note: θ , not 2θ). For the as-deposited nanowires, based on the first peak at 26.59 2 θ , a θ of 13.295 and a β of 0.074 were obtained, giving a 2 nm crystalline domain size. After annealing, using the same peak, a crystallite size of \sim 40 nm (β = 0.0037) was calculated.

Individual wires were studied by etching the alumina membranes in aqueous NaOH, followed by several centrifugation cycles in deionized water. Our previous experience with the template synthesis of nanowires shows that the forces involved in centrifugation can cause soft metal or polymer wires to bend and that polycrystalline semiconductor wires can break into fragments.¹⁴ After annealing, mostly intact CulnSe₂ wires were found in this study, indicating good mechanical integrity. Examination of the annealed nanowires with dark field TEM (Figure 2a,b) reveals crystallites of the size calculated from the XRD pattern.



Figure 2. Transmission electron micrographs of CulnSe₂ nanowires showing larger grains after annealing: (a) dark field image, (b) bright field image. (c) TEM/EDS map showing uniform elemental distribution.

The deleterious effects that small grains have on the performance of photovoltaic and photoelectrochemical cells are well documented in the literature. In a recent paper by Schierhorn and co-workers on CdSe nanorod array photoelectrodes they estimated that their as-electrodeposited rods had a grain density 1300 times that of annealed rods at 500 °C.³ Given that the crystallite sizes of their as-deposited and annealed rods are close to those reported here, a similar grain density is expected. Therefore electrical measurements were performed on the annealed nanowires.

$$L = \frac{0.94\lambda}{\beta\cos\theta_{\beta}} \tag{1}$$

Another important factor that can affect the performance of compound semiconductors is their stoichiometry and, in ternary and guaternary semiconductors in particular, phase separation. When electrodepositing CulnSe₂, it has been reported that the mechanism involves the formation of the binary phases Cu_xSe and In_ySe, which combine to form the ternary semiconductor.^{13,15,16} Nonstoichiometric deposits can result in the ternary phase with a binary phase as an impurity. The effects that these phase impurities have on the properties and performance of solar cells based on CulnSe₂ are reduction of the photoactive volume, increase in the boundary charge recombination, lower absorption coefficient, among others (for details see refs 17-19). From the XRD pattern it can be seen that within the detection limits there are no other phases present in our samples. This is further supported by TEM/EDS mapping, which shows a uniform elemental distribution throughout the wires (Figure 2c). Also the optical spectrum shows a transition around 1200 nm corresponding to a 1.0 eV band gap, which is in agreement with the value reported in the literature.18,20

To determine the stoichiometry of the nanowires, they were dissolved and the resulting solution was analyzed with ICP-MS (Table 1). Nanowires

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electrodeposited at $V_{\rm sce}$ = -600 mV were Cu-rich, whereas $V_{\rm sce} = -700$ mV gave stoichiometric wires that were slightly Cu-rich. Changing the electrodeposition potential by -50 mV also results in stoichiometric wires, but in this case slightly In-rich. These results are consistent with the electrodepostion of Cu, Se, and In at $V_{\rm sce} = -600 \text{ mV}$, $V_{\rm sce} = -700 \text{ mV}$, and $V_{\rm sce} = -800 \text{ mV}$ for Cu^{2+} -citrate complex, H_2SeO_3 , and In^{3+} -citrate complex, respectively.²¹⁻²³

It has been established that slightly Cu-rich CuInSe₂ is p-type, whereas slightly In-rich CuInSe₂ is n-type.^{24,25} To confirm that the samples deposited at V_{sce} = -700 mV and $V_{\text{sce}} = -750 \text{ mV}$ were p-type and n-type, respectively, Mott-Schottky (M-S) analysis was employed. The Mott-Schottky analysis is based on the relationship between the space charge layer capacitance and the applied potential, as given by eq 2. Here C is the capacitance of the space charge layer, e the elementary charge, ε the dielectric constant of the semiconductor, ε_{o} the permittivity, A the surface area, N the carrier concentration, V the applied potential, $V_{\rm fb}$ the flat band potential, k the Boltzmann constant, and T the temperature.^{26,27}

$$\frac{1}{C^2} = \left(\frac{2}{e\varepsilon\varepsilon_o NA^2}\right) \left(V - V_{\rm FB} - \frac{kT}{e}\right)$$
(2)

The M-S analysis allows one to determine $V_{\rm FB}$ by extrapolating to the intercept of the potential axis, and the carrier concentration can be calculated if the surface area is known. Also the slope (negative or positive) provides information on the carrier type. Figure 4a shows the M-S plot for a sample deposited at



deposition				
potential (vs SCE)	Cu (mol)	In (mol)	Se (mol)	stoichiometry
—600 mV —700 mV —750 mV	$\begin{array}{c} 6.77 \times 10^{-7} \\ 8.65 \times 10^{-7} \\ 6.77 \times 10^{-7} \end{array}$	$\begin{array}{c} 4.44 \times 10^{-7} \\ 8.27 \times 10^{-7} \\ 7.31 \times 10^{-7} \end{array}$	$\begin{array}{c} 4.18 \times 10^{-7} \\ 1.67 \times 10^{-6} \\ 1.45 \times 10^{-6} \end{array}$	1.60:1.06:1.00 1.04:1.00:2.02 1.00:1.08:2.14

 $V_{\rm sce} = -700$ mV. It can be seen that the plot has a negative slope, as expected for a p-type material, and $V_{\rm FB} pprox$ 15 mV, corresponding to the position of the valence band. For samples deposited at $V_{sce} = -750 \text{ mV}$ (Figure 4b) a positive slope is obtained, with a $V_{\rm FB} \approx -950$ mV, corresponding to the position of the conduction band. Both V_{FB} values are in agreement with values reported in the literature.²⁸⁻³⁰ Since the surface area is not known for our samples, we could not determine the carrier concentration by this method. However an estimate carrier concentration was obtained with electrical measurements.

It is important to mention that the electrical transport measurements were performed only on p-type wires because our future goal is to fabricate photovoltaic cells with a Mo/p-CuInSe₂NWs/CdS/ZnO configuration, which is the most common one for this material. First, the i-V properties of individual wires were measured while applying a gate voltage. The i-Vproperties were independent of the gate voltage applied, suggesting that the nanowires are heavily doped (Figure S4). Four-point probe measurements were also employed, and the results are presented in Table 2. The resistance, R, was determined from the slope of the i-V curve, and the resistivity, ρ , was calculated with dimensions obtained from SEM images. A hole mobility, $\mu_{\rm h}$, of 10 cm² V⁻¹ s⁻¹ was used to estimate the carrier concentrations.²⁵

From these measurements it was determined that most of the wires fall within a range of resistivity on the



Figure 3. Optical spectrum of drop-cast nanowires showing a transition close to 1200 nm, corresponding to a 1 eV band gap.



Figure 4. Mott-Schottky plots for CuInSe₂ nanowires film on Mo foil in 100 mM LiClO₄ in acetontrile, 10 mV ac signal, and 1kHz. Nanowires deposited at (a) V_{sce} = -700 mV and (b) V_{sce} = -750 mV.



TABLE 2. Results from Single-Wire Electrical Measurements

			carrier	
sample	R (ohm)	$ ho$ (ohm \cdot cm)	concentration (cm $^{-3}$)	
nanowire 1	$2.9 imes10^4$	2.1×10^{-1}	$3.0 imes10^{18}$	
nanowire 2	$2.02 imes 10^2$	1.1×10^{-3}	$5.5 imes10^{20}$	
nanowires 3-8	$(1-9.7) \times 10^{3}$	$(1-7.5) \times 10^{-2}$	$(2-8.4) \times 10^{19}$	



Figure 5. Single-wire four-point measurements with different contacts, showing ohmic behavior.

order of 10^{-2} ohm \cdot cm. The differences in the values obtained can be a result of different roughness, different level of defects, carrier mobility variations between wires, among other factors. However the estimated carrier concentrations indicate that the wires are heavily doped, which is consistent with the results obtained by gate-dependent measurements. Further, these values fall within those reported by several authors for electrodeposited films.^{24,31} In addition, Figure 5 illustrates that there is ohmic contact between the wires and the high work function metal electrodes (Pt, W), which is as expected for p-type wires. As a proof of concept, devices with the configuration Mo/p-CuInSe2NWs/ CdS/ZnO/ITO were made by drop-casting the nanowires onto Mo foil (see Supporting Information). The i-V power curves obtained from these devices show a photoresponse dominated by resistance (Figure S6). We attribute this to several factors. First, the sputtered top contacts were not continuous (Figure S5). Second, the sputtering conditions for the top contacts were not optimized, and thus the sputtered films were highly resistive. Finally, drop-casting results in a tortuous path for charge transport compared to vertically aligned nanowires. Future work will focus on improving these parameters for nanowire array solar cells.

CONCLUSIONS

CulnSe₂ nanowires were fabricated through a onestep electrodeposition process. The resulting wires are single-phase chalcopyrite and have a composition near the ideal 1:1:2 stoichiometry. The as-deposited nanowires have poor crystallinity, which readily improves upon annealing. The wires can be grown p- or n-type by adjusting the electrodeposition potential, which was confirmed by Mott–Schottky analysis. This analysis also allowed the determination of the flat band potential for both types of samples. Electrical measurements performed on individual wires revealed that the wires are heavily doped and have ohmic behavior when contacted with high work function metals, as expected for p-type samples. Future work will focus on making solar devices based on vertical nanowire arrays.

MATERIALS AND METHODS

Fabrication. Whatman Anodisc alumina membranes with a nominal pore diameter of 200 nm were used as templates. Previous experiments with metal and polymer electrodeposition in these membranes have shown that the pore diameter is actually ca. 350 nm, but that the pores are branched and narrower on one side of the membrane.¹⁴ A gold layer of 150 nm was thermally evaporated on one side of the membrane and served as the working electrode. Prior to the electrodeposition of CulnSe₂ a sacrificial layer of Au was electrodeposited at a constant current density of 0.67 mA/cm² for 1 h to fill the branched region of the membrane. A coiled platinum wire and a saturated calomel electrode (SCE) served as the counter and reference electrodes, respectively, when depositing the nanowires. The electrolyte used consisted of 3 mM Cu-SO₄ • 5H₂O, 3 mM In₂(SO₄)₃ • *n*H₂O, 5 mM SeO₂, and 0.1 M citric acid. The potential used was either -700 or -750 mV vs SCE. Once the CuInSe₂ nanowires were electrodeposited, the sacrificial Au backing and electrodeposited Au layer were removed using a commercial Au etchant (Transene GE-8148, 5 min). Afterward the samples were rinsed with copious amounts of water followed by ethanol. The samples were then annealed in a tube furnace at 400 °C for 15 min under nitrogen flow.

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Characterization. X-ray diffraction patterns were obtained using Cu $K\alpha_1$ radiation on a Philips X'pert MPD system while the wires were embedded in the template. The wires were released from the template for SEM/EDS (FEI Quanta 200 ESEM), TEM/EDS (JEOL 2010F), and optical characterization (Perkin-Elmer Lambda 950) by dissolving the alumina in 5 M NaOH and were cleaned with several cycles of centrifugation in H₂O until the supernatant had a neutral pH. For ICP-MS analysis (X-Series 2 ICPMS with Collision Cell Technology, Thermos, Fisher Scientific) the wires were dissolved in 143 μ L of concentrated HNO₃ and diluted to a final volume of 5 mL with H₂O to make a 2% HNO₃ matrix. Mott-Shottky analysis was performed by drop-casting the nanowires onto a molybdenum foil that was insulated with epoxy. The electrolyte used was 100 mM LiClO₄ in acetonitrile. the amplitude of the ac signal was 10 mV, and the frequency was 1 kHz. Platinum or tungsten electrical contacts to individual wires were made with a focused ion beam (FEI Quanta 200 3D). Nanowires were cast onto a n++ Si wafer coated with a 120 nm silicon nitride laver that served as the dielectric and a Ti(30 nm)/ Au(100 nm) layer on the other side that served as the back gate. For the electrical measurements, a Cascade Microtech Summit 12000 probe station was used coupled to two highimpedance Keithley 6514 System electrometers connected to the voltage measurement units of an Agilent 4516B semiconductor parameter analyzer.



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Supporting Information Available: Scanning electron microscope images and EDS spectra of CulnSe₂ nanowires, SEM images of individual wires aligned for four-point conductivity measurements, conductivity data, fabrication details of thin film photovoltaic devices, and electrical characterization. This material is available free of charge *via* the Internet at http://pubs.acs.org.

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